

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12
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1/17/02

Application Serial No. 09/502,693
Filing Date February 11, 2000
Inventor James E. O'Toole et al.
Assignee Micron Technology, Inc.
Group Art Unit 3662
Examiner Unknown
Attorney's Docket No. MI40-273
Title: Method of Speeding Power-Up of an Amplifier, and Amplifier



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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References -- See Attached Form PTO-1449

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The attached form PTO-1449 is submitted in compliance with 37 C.F.R. §1.56. No admission is made regarding whether the submitted reference is prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. Section 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Dated: 10-17-01

Attorney: D. Brent Kenady
D. Brent Kenady
Reg. No. 40,045

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36621

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PTO/SB/21 (6-98)
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/502,693
	Filing Date	February 11, 2000
	First Named Inventor	James E. O'Toole et al.
	Group Art Unit	3662
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	MI40-273

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	D. Brent Kenady Reg. No. 40,045; Wells, St. John, P.S.
Signature	
Date	10-17-01

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 10-19-01	
Typed or printed name	Connie L. Katzman
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI40-273	SERIAL NO. 09/502,693	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Micron Technology, Inc.		
				FILING DATE February 11, 2000	GROUP 3662	
U.S. PATENT DOCUMENTS						
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
W	AA	5,780,916	7/98	Berger et al.	257	471
W	AB	5,143,820	09/92	Kotecha et al.	430	314
W	AC	4,478,881	10/84	Bartur et al.	427	90
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Subclass	Translation
						Yes No
	AL					
	AM					
	AN					
	AO					
	AP					
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)						
W	AR		Fink & Christiansen, ELECTRONIC ENGINEER'S HANDBOOK, 3rd Edition, McGraw-Hill, 1989, pgs. 8-22, 8-23 and 8-40.			
W	AS		Wolf, SILICON PROCESSING FOR THE VLSI ERA VOLUME 2: PROCESS INTEGRATION, 1986 Lattice Press, pgs. 65 and 195.			
	AT					
EXAMINER			DATE CONSIDERED			

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